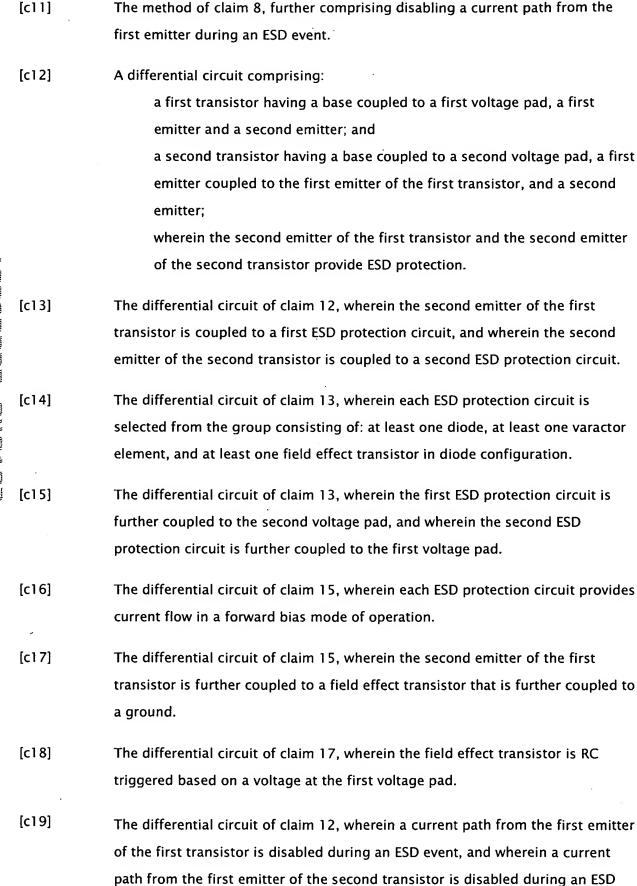
	[c1]	A transistor comprising:
H. grows grows array every press. 11 th fills of a 1 i strange and in the fills of a 1 i strange and in t		a first emitter for providing bipolar operation; and
		a second emitter for providing ESD protection.
	[c2]	The transistor of claim 1, further including an ESD protection circuit coupled to
		the second emitter and further coupled to one of the group consisting of: a
		voltage pad, a ground, a ground plane, and a base of a transistor.
	[c3]	The transistor of claim 2, wherein the ESD protection circuit comprises at least
	•	one diode.
	[c4]	The transistor of claim 3, wherein each diode is selected from the group
		consisting of: a schottky diode, a Zener diode, a SiGe npn in diode connection, a
		varactor element, and a field effect transistor in diode configuration.
	[c5]	The transistor of claim 2, wherein the ESD protection circuit comprises at least
		one field effect transistor.
	[c6]	The transistor of claim 1, further including a circuit for disabling a current path
		for the first emitter during an ESD event.
	[c7]	The transistor of claim 6, wherein the disabling circuit includes a field effect
		transistor.
	[c8]	A method of providing ESD protection comprising:
		providing a transistor having a first emitter and a second emitter; and
		coupling the second emitter to an ESD protection circuit.
	[c9]	The method of claim 8, further comprising:
		coupling the ESD protection circuit to a voltage pad;
		disabling a current path from the second emitter to the voltage pad
		during normal operation; and
		enabling the current path during an ESD event.
	[c10]	The method of claim 8, further comprising enabling a current path from the

second emitter to a ground during an ESD event.

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event.

[c20]

The differential circuit of claim 19, wherein a first field effect transistor disables the current path from the first emitter of the first transistor, and wherein a second field effect transistor disables the current path from the first emitter of the second transistor.